OIPE NO 167	May 20 TO II	N THE UNITED STATES F	PATEN	T AND TRAI	DEMARK OFFICE
TENT 8 TR	Re the App	lication of:)		
	Anand	S. Murthy et al.)	Art Unit:	2823
	Serial No.:	09/831,539)	Examiner:	Kebede, Brook
	Filed:	April 15, 2002)		
	For: METHO	DD OF FABRICATING A FIELD)		
		T TRANSISTOR STRUCTURE ABRUPT SOURCE/DRAIN)		
	JUNCTIONS (AS)		
	Mail Stop ISS		_		
	Commissione	r for Patents			
	P.O. Box 145	n .			

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Sir:

Alexandria, VA 22313-1450

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on

August 10, 2004
(Date of Deposit)

Claire Wallters

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09/831,539 -1-

42390.P6624US Patent

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fees in connection with this communication to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

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Substitute f	or Form 1449/1	РТО	13.		Complete	if Kn	own	
	INIEOI	D N A A	TI ÖNDIS ËLOSU.	DE	Application Number	09/	831,539	
	ШТО	CIVIA	THOMOS CLUSU.	KL .	Filing Date	Apı	ril 15, 2002	
	STAT	EMI	ENT BY APPLICAL	NT	First Named Inventor:		Anand S. Murthy	
		(use	Art Unit	2823				
					Examiner Name	+	bede, Brook	
Sheet	1		of 2		Attorney Docket Number	1	390.P6624US	
			U.S. PATENT	DOCUMENTS				
Examiner Initials*	Cite No.1	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant	
		Numl	per-Kind Code ² (If known)				Passages or Relevant Figures Appear	
		US-	4,963,502	10-16-1990	Teng et al.			
		US-	5,949,116	09-07-1999	Wen			
		US-	5,956,590	09-21-1999	Hsieh et al.			
		US-	6,010,936	01-04-2000	Son			
		US-	6,022,783	02-08-2000	Wu			
		US-	6,025,235	02-15-2000	Krivokapic			
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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
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Examiner	Date Considered	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Co	mplete if Known	
Application Number	09/831,539	
Filing Date	April 15, 2002	
First Named Inventor:	Anand S. Murthy	
Art Unit	2823	
Examiner Name	Kebede, Brook	
Attorney Docket Number	42300 P6624US	

Sheet	2		of 2		Attorney Docket Number	42390.P6624US	
				NON PATENT LIT	TERATURE DOCUMENT	S	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published					
		S. WO	LF, ET AL tice Press,	., Silicon Processing Sunset Beach, CA 90	For The VLSI Era, Volume 0742, U.S.A., pgs. 171-172,	1: Process Technology, © 1986 by 542-544, 548-555.	
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